

Parrot v1.1.2

MSP430 dev board
Parrot 1.1.2 - Top Sheet SchDoc

Designer : GS/GH/AE
Date : 14 May 2014
Board revision : V1.1.2
Board description : MSP430 Development PCB

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Revision History

Revision number		Date	Engineer	Description
Major	Minor			
V1.1	0	21/08/13		- JTAG changed to 2.54mm pitch (was 2mm) - XT2 changed to smaller footprint - see wiki for further changes
V1.1	1	11-04-14		- fixed USB stability issue (C18, C19 = 22pF) - I2C connection to USB section removed (R40, R41) - I2C pull ups removed. Pull ups now downstream only (R27, R28)
V1.1	2	14/05/2014	GSS	Further change to USB Stability. C25 470nF=>1µF, 0603. Delays exit from RESET on TUSB3410.

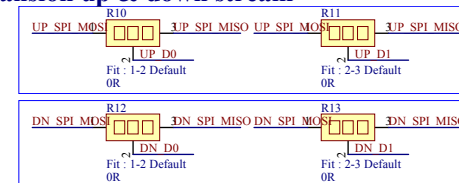
A



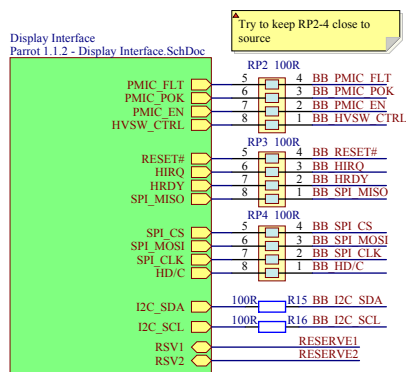
Connects to Ruddock2 P9

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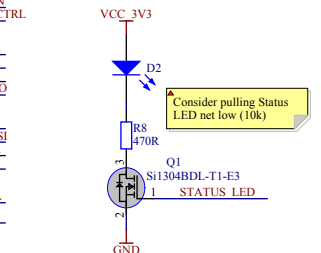
"Reset" sw also fitted on Ruddock2



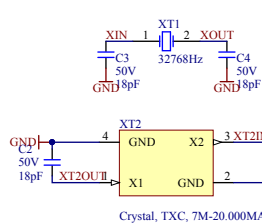
Test Point header pads, each side of U1



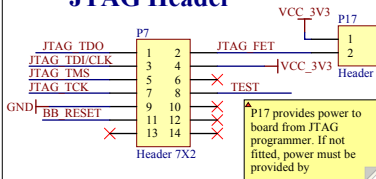
User LED/ Switch



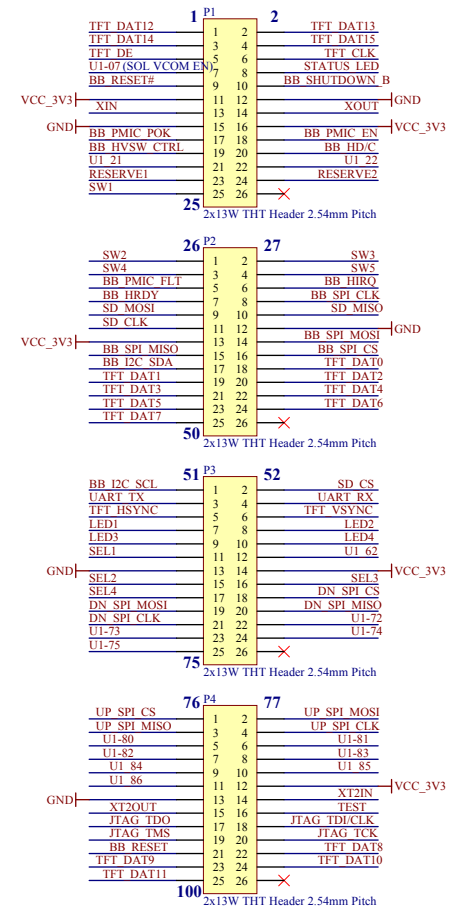
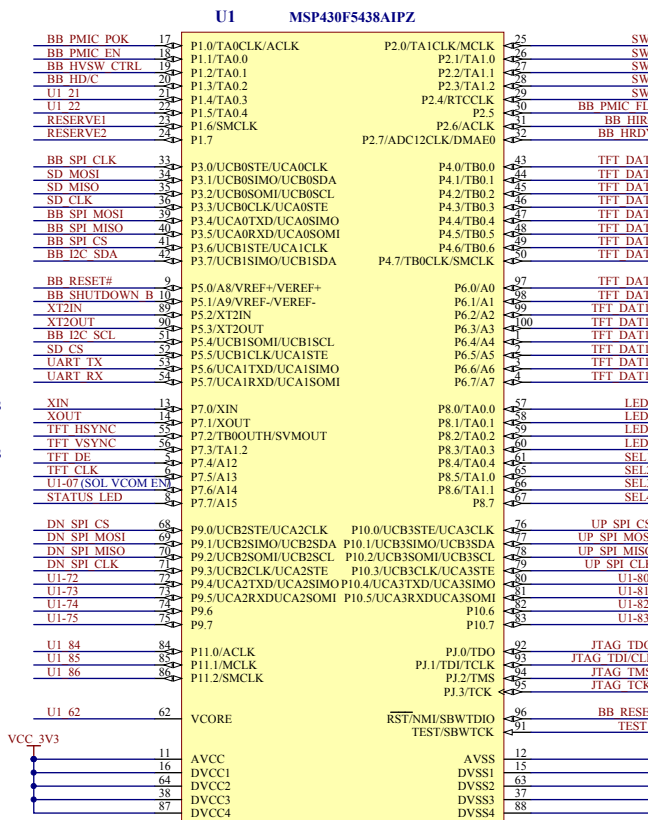
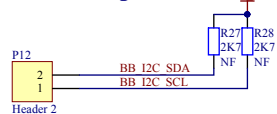
Clocks



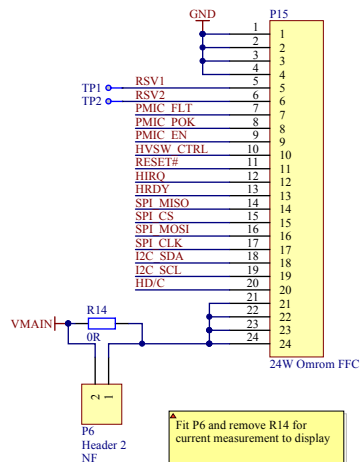
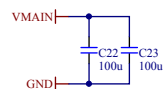
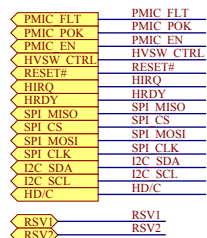
JTAG Header

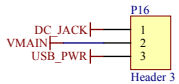
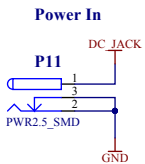


I2C Expansion



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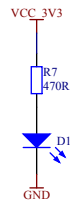


▲ Set P16 to select 5v from usb port, or dc jack/ expansion header.

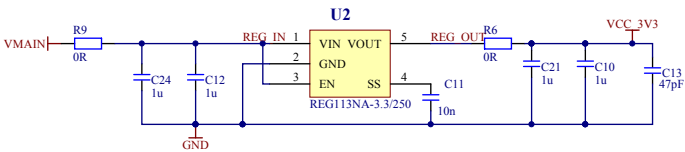
Use P16 to check current on 5V rail.

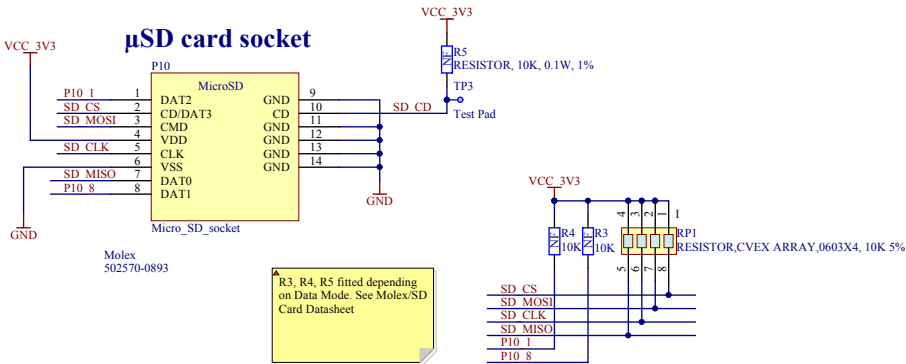
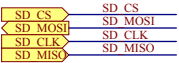
▲ If PSU socket IS used, Vmain is provided to Ruddock2 and MSP430 board from this socket. BB_3V3 is provided to both boards.

If PSU socket is NOT used, then Ruddock2 will be powered, and Vmain is provided from Ruddock to the MSP430 board. 3V3 is then generated on the MSP430 board, and fed back to the Ruddock2. Ruddock2 has no local generation of 3V3



3V3 Regulator





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